

Response: 10/738,498

Attorney Docket: P17255

Amendments to the Specification

Please replace paragraph 0020 of the Specification with the following amended paragraph.

[0020] While in SMX mode, Integrity Monitor 105 may also impose restrictions on certain areas of PC 100's memory and designate those areas as non-writable, i.e., protected memory, unavailable to DMA access by input/output ("I/O") devices and/or software running on PC 100. In one embodiment of the present invention, certain components of Guest Software 150 may be placed into this area of non-writable memory, which provides them with yet another layer of protection against tampering. For example, the kernel code on PC 100, which is unlikely to change during runtime, may be executed in this non-writable memory area.